

 INFORMATION DISCLOSURE STATEMENT BY APPLICANT  				<i>Complete if Known</i>
Sheet 1 of 1				Attorney Docket Number 20040084 US
Application Number 10/593,807				Filing Date 9/21/06
First Named Inventor Turner, Steven E.				Group Art Unit
Examiner Name				

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

Examiner Signature		Date Considered	
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* EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /TM/

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NON PATENT LITERATURE DOCUMENTS					
Examiner Initials	Cite No.	(Including Name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc...), date, page(s), volume-issue number(s), publisher, city, and/or country where published.)			
		TURNER, ET AL., Benchmark Results For High-Speed 4-Bit Accumulators Implemented In Indium Phosphide DHBT Technology, IEEE Lester Eastman Conference on High Performance Devices, Rensselaer Polytechnic Institute, August 4-6, 2004			T
		GUTIERREZ-AITKEN, ET AL., Ultrahigh-Speed Direct Digital Synthesizer Using InP DHBT Technology, IEEE Journal of Solid-State Circuits, Vol. 37, No. 9, September 2002, pp 1115-1119			
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		EKROOT, ET AL., A GaAs 4-bit Adder-Accumulator Circuit for Direct Digital Synthesis, IEEE Journal of Solid-State Circuits, Vol. 23, No. 2, April 1988, pp 573-580			
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		SALOUS, ET AL., FPGA-based Hybrid Accumulator Architecture for Digital Chirp Synthesis, Int. J. Electronics, 1996, Vol. 80, No. 3, pp 441-447			
		BETOWSKI, ET AL., Considerations for Phase Accumulator Design for Direct Digital Frequency Synthesizers, School of Electrical Engineering & Computer Science, Washington State University, Pullman, WA 99164			
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Examiner Signature	/Tan Mai/	Date Considered	09/24/2008
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